**Practical Exam LICD**

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**ETRX-B1**

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| **Aim and Objective of the Experiment:** |
| Implement subtractor using IC 741 |

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| **Circuit Diagram:** |
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| **Calculation:** |
| **Design:-**    **Theoretical Calculation:** |
| **Observation Table:** |
| **Minimum 3 readings**   |  |  |  |  |  | | --- | --- | --- | --- | --- | | **Sr no** | **Va** | **Vb** | **Vout(Theoretical)** | **Vout(Simulated)** | | **1** | **9** | **4** | **5V** | **4.99999V** | | **2** | **5** | **7** | **-2V** | **-1.99999V** | | **3** | **8** | **4** | **4V** | **3.99999V** | | **4** | **-2** | **4** | **-6V** | **-5.99998V** | | **5** | **-4** | **-6** | **2V** | **1.99999V** | |

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| **Results:**  **Reading 1** |
| **Reading 2**    **Reading 3**    **Reading 4**    **Reading 5** |

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| **Conclusion:** |
| I have successfully Designed & Implemented Subtractor using OPAMP using LTSPICE.  Here, difference of the input voltages applied at its inverting and non-inverting terminals can be seen at the output terminal.  We have also compared theoretical and practical values |